

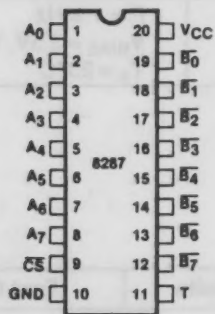
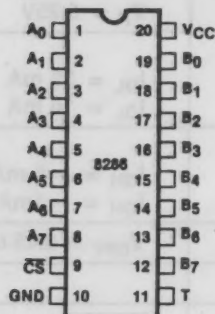


8286/8287 8-BIT PARALLEL BIDIRECTIONAL BUS DRIVERS

- Data Bus Buffer Driver for MCS-86, MCS-80, MCS-85, and MCS-48
- High Output Drive Capability for Driving System Data Bus
- Fully Parallel 8-Bit Transceivers
- 3-State Outputs
- 20-Pin Package with 0.3" Center
- No Output Low Noise when Entering or Leaving High Impedance State

The 8286 and 8287 are 8-bit bipolar transceivers with 3-state outputs. The 8287 inverts the input data at its outputs while the 8286 does not. Thus, a wide variety of applications for buffering in microcomputer systems can be met.

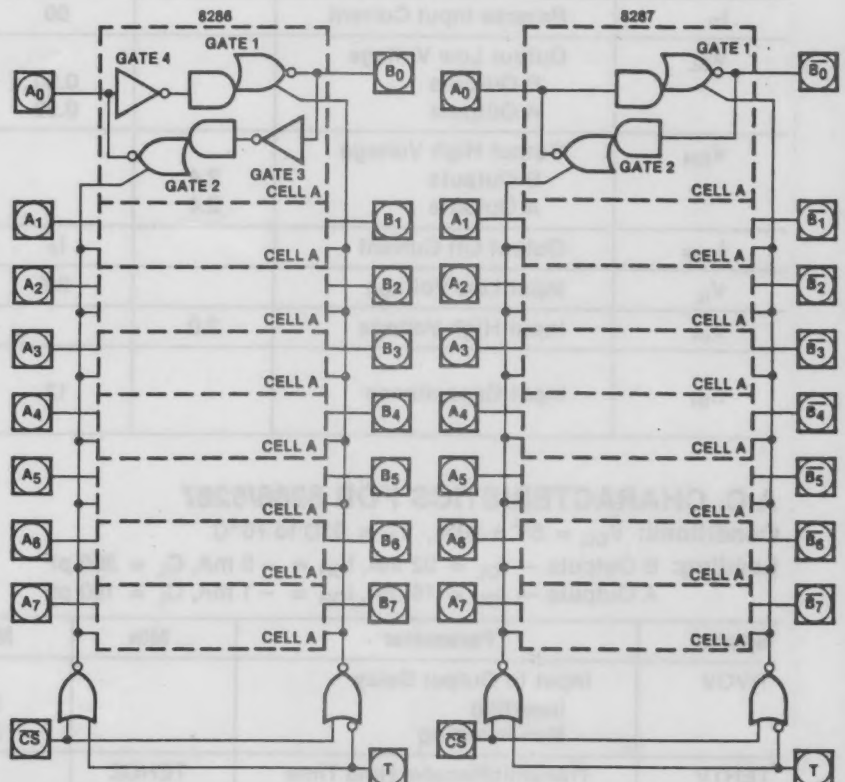
PIN CONFIGURATIONS



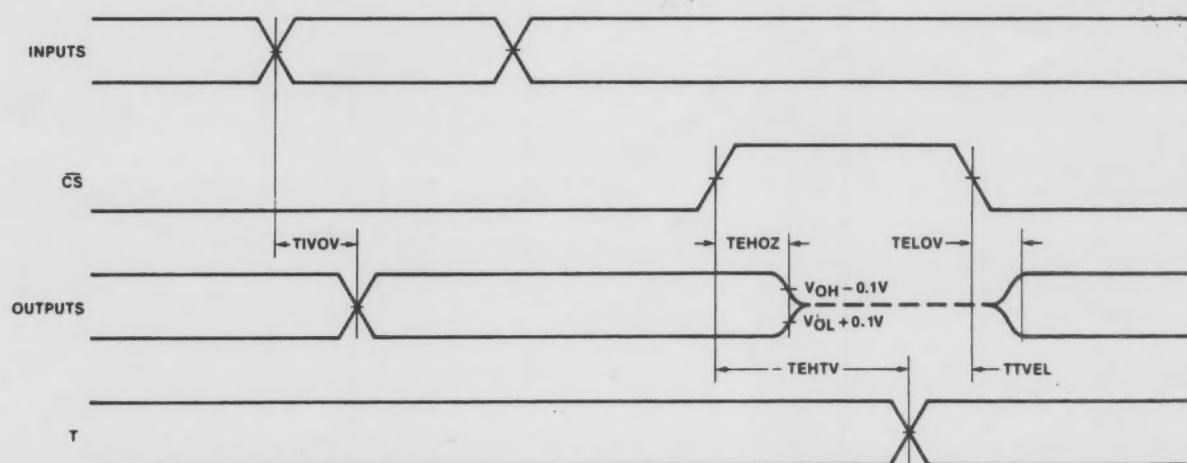
PIN NAMES

A ₀ -A ₇	LOCAL BUS DATA
B ₀ -B ₇	SYSTEM BUS DATA
CS	CHIP SELECT
T	TRANSMIT

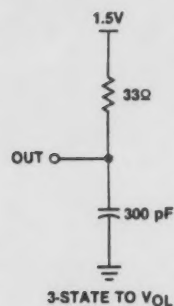
LOGIC DIAGRAMS



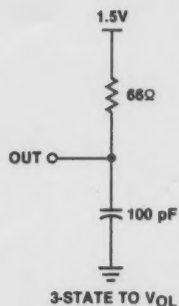
8286/8287 TIMING



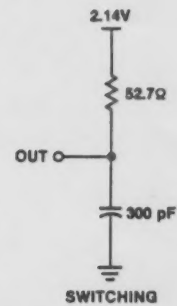
TEST LOAD CIRCUITS



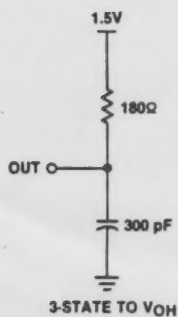
B OUTPUT



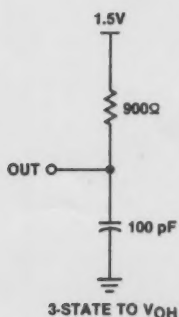
A OUTPUT



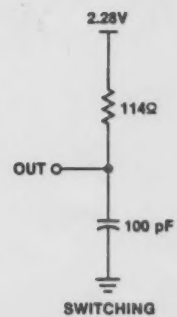
B OUTPUT



B OUTPUT



A OUTPUT



A OUTPUT